TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

ULN2003AP,ULN2003AFW,ULN2004AP,ULN2004AFW (Manufactured by Toshiba Malaysia)

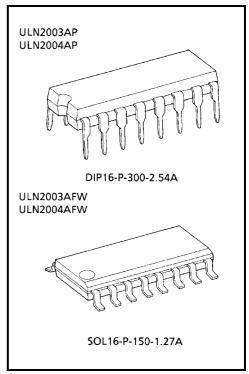
7CH DARLINGTON SINK DRIVER

The ULN2003AP / AFW Series are high-voltage, high-current darlington drivers comprised of seven NPN darlington pairs. All units feature integral clamp diodes for switching inductive loads.

Applications include relay, hammer, lamp and display (LED) drivers.

FEATURES

- Output current (single output) 500 mA MAX.
- High sustaining voltage output 50 V MIN.
- Output clamp diodes
- Inputs compatible with various types of logic
- Package Type-AP : DIP-16pinPackage Type-AFW : SOL-16pin

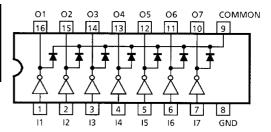


Weight

DIP16-P-300-2.54A : 1.11 g (Typ.) SOL16-P-150-1.27A : 0.15 g (Typ.)

PIN CONNECTION (TOP VIEW)

TYPE	INPUT BASE RESISTOR	DESIGNATION
ULN2003AP / AFW	2.7 kΩ	TTL, 5 V CMOS
ULN2004AP / AFW	10.5 kΩ	6~15 V PMOS, CMOS



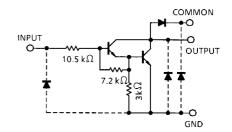


SCHEMATICS (EACH DRIVER)

ULN2003AP / AFW

COMMON OUTPUT 7.2 kΩ C M GND

ULN2004AP / AFW



Note: The input and output parasitic diodes cannot be used as clamp diodes.

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT	
Output Sustaining Voltage		V _{CE (SUS)}	-0.5~50	V	
Output Current		lout	500	mA / ch	
Input Voltage		V _{IN}	-0.5~30	V	
Clamp Diode Reverse Voltage		V _R	50	٧	
Clamp Diode Forward Current		l _F	500	mA	
	AP		1.47	W	
Power Dissipation	AFW	P _D	0.54 / 0.625 (Note)		
Operating Temperature		T _{opr}	-40~85	°C	
Storage Temperature		T _{stg}	tg -55~150		

Note: On glass epoxy PCB (30 × 30 × 1.6 mm Cu 50%)



RECOMMENDED OPERATING CONDITIONS (Ta = -40~85°C)

CHARACTE	ERISTIC	SYMBOL	TEST CONDITION		MIN	TYP.	MAX	UNIT	
Output Sustaining Volta	utput Sustaining Voltage V _{CE (SUS)}			0	_	50	V		
Output Current	AP	- Іоит	T _{pw} = 25 ms	Duty = 10%	0	_	370		
			7 Circuits	Duty = 50%	0	_	130	mA / ch	
	AFW		Ta = 85°C	Duty = 10%	0	_	233		
			T _j = 120°C	Duty = 50%	0	_	70		
Input Voltage		V _{IN}			0	_	24	V	
Input Voltage (Output On)	ULN2003A	V	I _{OUT} = 400 mA h _{FE} = 800		2.8	_	24	V	
	ULN2004A	V _{IN} (ON)			6.2	_	24		
Input Voltage (Output Off)	ULN2003A	V			0	_	0.7	V	
	ULN2004A	V _{IN} (OFF)			0	_	1.0	V	
Clamp Diode Reverse Voltage		V _R			_	_	50	V	
Clamp Diode Forward Current		I _F			_	_	350	mA	
Power Dissipation	AP	D-	Ta = 85°C		_	_	0.76	W	
	AFW	P _D	Ta = 85°C	(Note)	_	_	0.325	VV	

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Note: On glass epoxy PCB (30 × 30 × 1.6 mm Cu 50%)

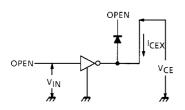


ELECTRICAL CHARACTERISTICS (Ta = 25°C unless otherwise noted)

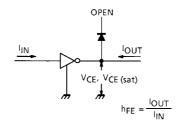
CHARACTE	RISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION		MIN	TYP.	MAX	UNIT
Output Lasks as Output		1	1	V _{CE} = 50 V, Ta = 25°C		_	_	50	
Output Leakage Current		I _{CEX}		V _{CE} = 50 V, Ta = 85°C		_	_	100	μA
Collector-Emitter Saturation Voltage			2	I _{OUT} = 350 mA, I _{IN} = 500 μA		_	1.3	1.6	٧
		V _{CE (sat)}		I _{OUT} = 200 r	I _{OUT} = 200 mA, I _{IN} = 350 μA		1.1	1.3	
				I _{OUT} = 100 mA, I _{IN} = 250 μA		_	0.9	1.1	
DC Current Transfer I	Ratio	h _{FE}	2	V _{CE} = 2 V, I _{OUT} = 350 mA		1000	_	_	
Input Current	ULN2003A		_	V _{IN} = 2.4 V, I _{OUT} = 350 mA		_	0.4	0.7	mA
(Output On)	ULN2004A	I _{IN (ON)}	3	V _{IN} = 9.5 V, I _{OUT} = 350 mA		_	0.8	1.2	
Input Current (Output Off)		I _{IN (OFF)}	4	I _{OUT} = 500 μA, Ta = 85°C		50	65	_	μA
	ULN2003A	Vin (ON)	5	V _{CE} = 2 V h _{FE} = 800	I _{OUT} = 350 mA	_	_	2.6	V
Input Voltage					I _{OUT} = 200 mA	_	_	2.0	
(Output On)	ULN2004A				I _{OUT} = 350 mA	_	_	4.7	
					I _{OUT} = 200 mA	_	_	4.4	
Clamp Diode Reverse Current			6	V _R = 50 V, Ta = 25°C		_	_	50	
		I _R		V _R = 50 V, Ta = 85°C		_	_	100	μA
Clamp Diode Forward Voltage		V _F	7	I _F = 350 mA		_	_	2.0	V
Input Capacitance		C _{IN}	_			_	15	_	pF
Turn-On Delay		ton	8	V_{OUT} = 50 V, R_{L} = 125 Ω C_{L} = 15 pF		_	0.1	_	us
Turn-Off Delay		t _{OFF}	8	V_{OUT} = 50 V, R_{L} = 125 Ω C_{L} = 15 pF		_	0.2	_	μσ

TEST CIRCUIT

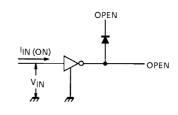
1. I_{CEX}



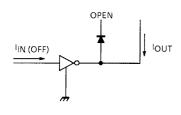
2. V_{CE (sat),} h_{FE}



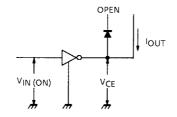
3. I_{IN (ON)}



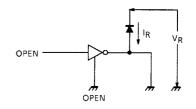
4. In (OFF)



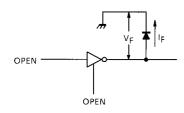
5. V_{IN (ON)}



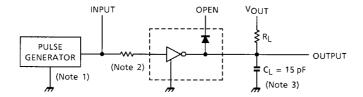
6. I_R

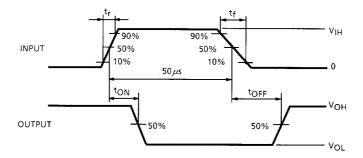


7. V_F



8. ton, toff





Note 1: Pulse width 50 µs, duty cycle 10%

Output impedance 50 Ω , $t_f \le 5$ ns, $t_f \le 10$ ns

Note 2: See below

INPUT CONDITION

TYPE NUMBER	R1	V _{IH}					
ULN2003AP / AFW	0	3 V					
ULN2004AP / AFW	0	8 V					

Note 3: C_L includes probe and jig capacitance.

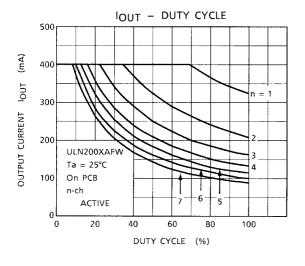
PRECAUTIONS for USING

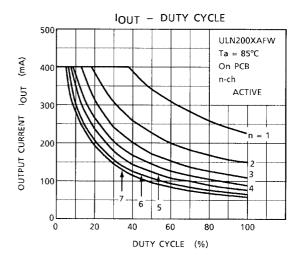
This IC does not include built-in protection circuits for excess current or overvoltage.

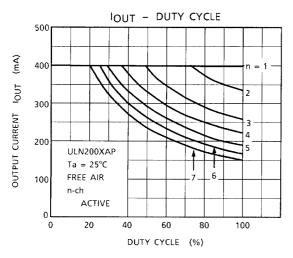
If this IC is subjected to excess current or overvoltage, it may be destroyed.

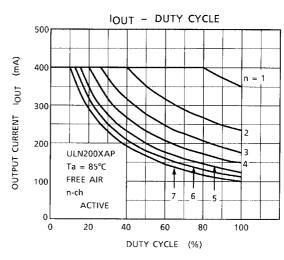
Hence, the utmost care must be taken when systems which incorporate this IC are designed.

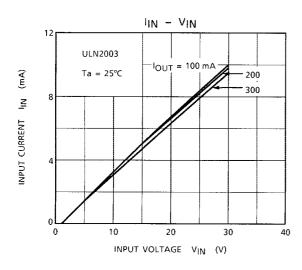
Utmost care is necessary in the design of the output line, COMMON and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

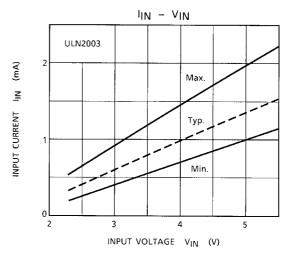


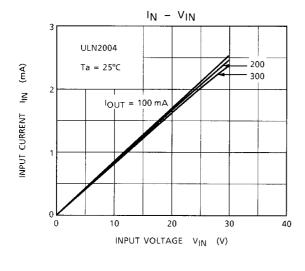


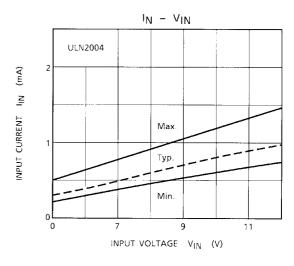


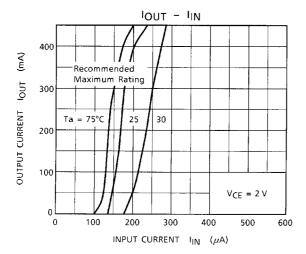


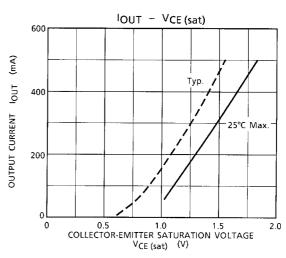


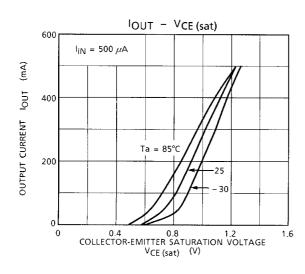


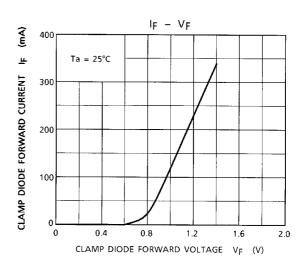


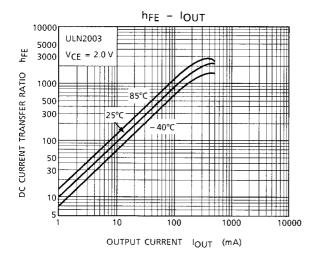


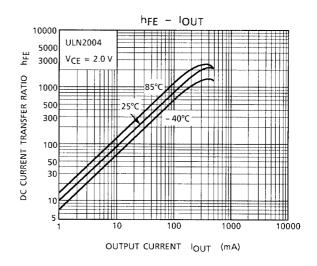


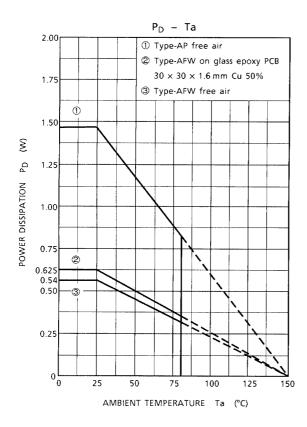








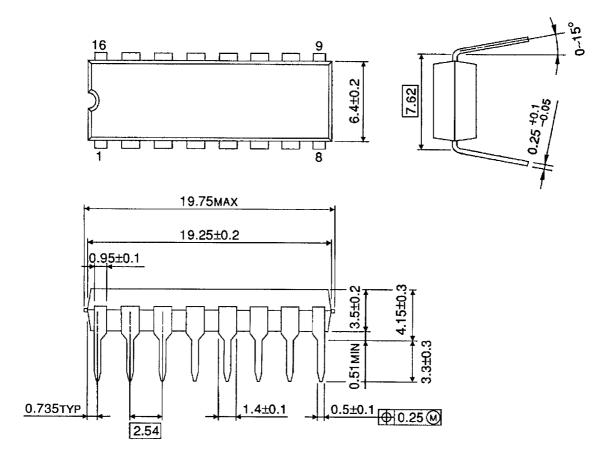






PACKAGE DIMENSIONS

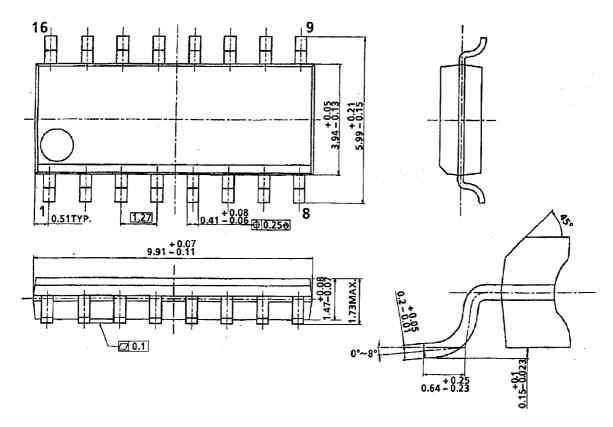
DIP16-P-300-2.54A



Weight: 1.11 g (Typ.)

PACKAGE DIMENSIONS

SOL16-P-150-1.27A



Weight: 0.15 g (Typ.)

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000707EBA

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