

# μA7800 SERIES

## THREE - TERMINAL POSITIVE VOLTAGE REGULATORS

### FAIRCHILD LINEAR INTEGRATED CIRCUITS

**GENERAL DESCRIPTION** — The μA7800 series of Three-Terminal Positive Voltage Regulators are constructed using the Fairchild Planar\* epitaxial process. These regulators employ internal current limiting, thermal shutdown and safe-area compensation making them essentially blow-out proof. If adequate heat sinking is provided, they can deliver over 1A output current. They are intended as fixed-voltage regulators in a wide range of applications including local, on-card regulation for elimination of noise and distribution problems associated with single point regulation. In addition to use as fixed voltage regulators, these devices can be used with external components to obtain adjustable output voltages and currents and as the power pass element in precision regulators.

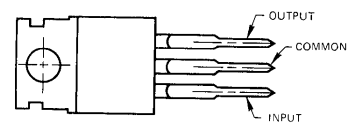
- OUTPUT CURRENT IN EXCESS OF 1 AMP
- NO EXTERNAL COMPONENTS
- INTERNAL THERMAL OVERLOAD PROTECTION
- INTERNAL SHORT CIRCUIT CURRENT LIMITING
- OUTPUT TRANSISTOR SAFE-AREA COMPENSATION
- AVAILABLE IN THE PLASTIC TO-220 AND THE METAL TO-3 PACKAGE

**ABSOLUTE MAXIMUM RATINGS**

Input Voltage (5 V through 18 V)	35 V
(24 V)	40 V
Internal Power Dissipation (Note 1)	Internally Limited
Storage Temperature Range	-65° C to +150° C
Operating Junction Temperature Range	0° C to +125° C
Lead Temperature (Soldering, 60 second time limit) TO-3 Package	300° C
(Soldering, 10 second time limit) TO-220 Package	230° C

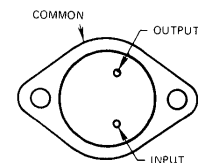
VOLTAGE RANGE	
μA7805	5 V
μA7806	6 V
μA7808	8 V
μA7812	12 V
μA7815	15 V
μA7818	18 V
μA7824	24 V

**CONNECTION DIAGRAMS**  
**TO-220 PLASTIC POWER PACKAGE**  
(TOP VIEW)

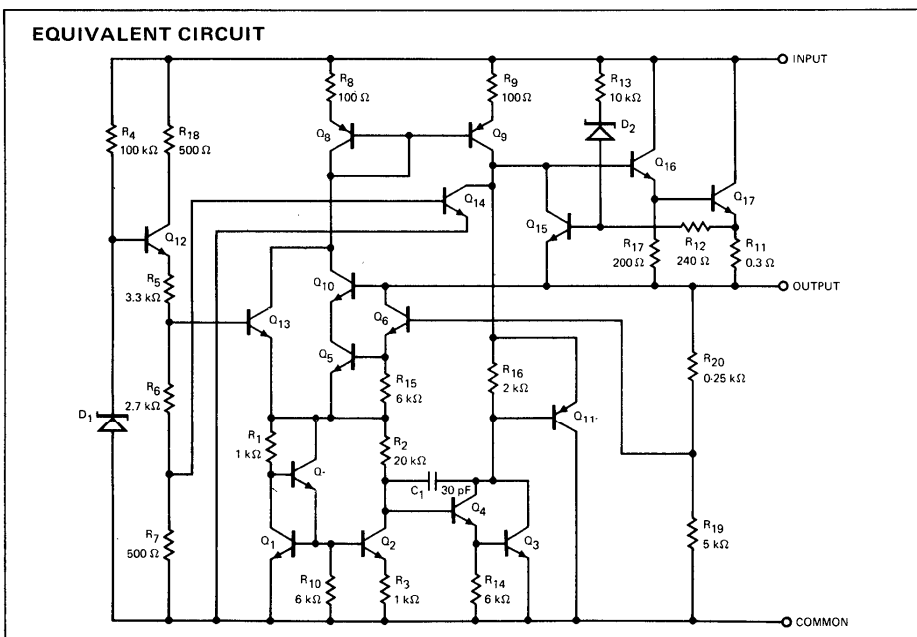


- ORDER PART NOS:** UGH7805393  
 UGH7806393  
 UGH7808393  
 UGH7812393  
 UGH7815393  
 UGH7818393  
 UGH7824393

**TO-3 PACKAGE**  
(TOP VIEW)



- ORDER PART NOS:** UGJ7805393  
 UGJ7806393  
 UGJ7808393  
 UGJ7812393  
 UGJ7815393  
 UGJ7818393  
 UGJ7824393



Note on following page.

\*Planar is a patented Fairchild process.

FAIRCHILD LINEAR INTEGRATED CIRCUITS •  $\mu$ A7800 SERIES

$\mu$ A7805

ELECTRICAL CHARACTERISTICS ( $V_{IN} = 10\text{ V}$ ,  $I_{OUT} = 500\text{ mA}$ ,  $0^\circ\text{C} < T_J < 125^\circ\text{C}$ , unless otherwise specified)

PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Output Voltage	$T_J = 25^\circ\text{C}$	4.8	5.0	5.2	V
Line Regulation	$T_J = 25^\circ\text{C}$ , $I_{OUT} = 100\text{ mA}$ $7\text{ V} \leq V_{IN} \leq 25\text{ V}$		7.0	50	mV
	$8\text{ V} \leq V_{IN} \leq 12\text{ V}$		2.0	25	mV
	$T_J = 25^\circ\text{C}$ , $I_{OUT} = 500\text{ mA}$ $7\text{ V} \leq V_{IN} \leq 25\text{ V}$		35	100	mV
	$8\text{ V} \leq V_{IN} \leq 12\text{ V}$		8.0	50	mV
Load Regulation	$T_J = 25^\circ\text{C}$ , $5\text{ mA} \leq I_{OUT} \leq 1.5\text{ A}$ $250\text{ mA} \leq I_{OUT} \leq 750\text{ mA}$		11	100	mV
Output Voltage	$7\text{ V} \leq V_{IN} \leq 20\text{ V}$ , $5\text{ mA} \leq I_{OUT} \leq 1.0\text{ A}$ $p \leq 15\text{ W}$	4.75		5.25	V
Quiescent Current	$T_J = 25^\circ\text{C}$		4.3	8.0	mA
Quiescent Current Change	$7\text{ V} \leq V_{IN} \leq 25\text{ V}$ $5\text{ mA} \leq I_{OUT} \leq 1.5\text{ A}$			1.3	mA
				0.5	mA
Output Noise Voltage	$T_A = 25^\circ\text{C}$ , $10\text{ Hz} \leq f \leq 100\text{ kHz}$		40		$\mu\text{V}$
Long Term Stability				20	mV
Ripple Rejection	$I_{OUT} = 20\text{ mA}$ , $f = 120\text{ Hz}$		70		dB
Dropout Voltage	$I_{OUT} = 1\text{ A}$ , $T_J = 25^\circ\text{C}$		2.0		V
Output Resistance			30		$\text{m}\Omega$
Short Circuit Current Limit	$T_J = 25^\circ\text{C}$		750		mA
Average Temperature Coefficient of Output Voltage	$I_{OUT} = 5\text{ mA}$ , $0^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$		-1.3		$\text{mV}/^\circ\text{C}$

$\mu$ A7806

ELECTRICAL CHARACTERISTICS ( $V_{IN} = 11\text{ V}$ ,  $I_{OUT} = 500\text{ mA}$ ,  $0^\circ\text{C} < T_J < 125^\circ\text{C}$ , unless otherwise specified)

PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Output Voltage	$T_J = 25^\circ\text{C}$	5.75	6.0	6.25	V
Line Regulation	$T_J = 25^\circ\text{C}$ , $I_{OUT} = 100\text{ mA}$ $8\text{ V} \leq V_{IN} \leq 25\text{ V}$		9.0	60	mV
	$9\text{ V} \leq V_{IN} \leq 13\text{ V}$		3.0	30	mV
	$T_J = 25^\circ\text{C}$ , $I_{OUT} = 500\text{ mA}$ $8\text{ V} \leq V_{IN} \leq 25\text{ V}$		43	120	mV
	$9\text{ V} \leq V_{IN} \leq 13\text{ V}$		10	60	mV
Load Regulation	$T_J = 25^\circ\text{C}$ , $5\text{ mA} \leq I_{OUT} \leq 1.5\text{ A}$ $250\text{ mA} \leq I_{OUT} \leq 750\text{ mA}$		13	120	mV
Output Voltage	$8\text{ V} \leq V_{IN} \leq 21\text{ V}$ , $5\text{ mA} \leq I_{OUT} \leq 1.0\text{ A}$ $p \leq 15\text{ W}$	5.7		6.3	V
Quiescent Current	$T_J = 25^\circ\text{C}$		4.3	8.0	mA
Quiescent Current Change	$8\text{ V} \leq V_{IN} \leq 25\text{ V}$ $5\text{ mA} \leq I_{OUT} \leq 1.5\text{ A}$			1.3	mA
				0.5	mA
Output Noise Voltage	$T_A = 25^\circ\text{C}$ , $10\text{ Hz} \leq f \leq 100\text{ kHz}$		45		$\mu\text{V}$
Long Term Stability				24	mV
Ripple Rejection	$I_{OUT} = 20\text{ mA}$ , $f = 120\text{ Hz}$		65		dB
Dropout Voltage	$I_{OUT} = 1\text{ A}$ , $T_J = 25^\circ\text{C}$		2.0		V
Output Resistance	$I_{OUT} = 500\text{ mA}$		35		$\text{m}\Omega$
Short Circuit Current Limit	$T_J = 25^\circ\text{C}$		550		mA
Average Temperature Coefficient of Output Voltage	$I_{OUT} = 5\text{ mA}$ , $0^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$		-1.0		$\text{mV}/^\circ\text{C}$

NOTE 1. Thermal resistance without a heat sink for junction to case temperature is  $4.0^\circ\text{C}/\text{W}$  for TO-3 package,  $2.0^\circ\text{C}/\text{W}$  for TO-220 package; ambient to case temperature is  $35^\circ\text{C}/\text{W}$  for TO-3 package and  $50^\circ\text{C}/\text{W}$  for TO-220 package.

**FAIRCHILD LINEAR INTEGRATED CIRCUITS •  $\mu$ A7800 SERIES**

**$\mu$ A7808**

**ELECTRICAL CHARACTERISTICS** ( $V_{IN} = 14\text{ V}$ ,  $I_{OUT} = 500\text{ mA}$ ,  $0^\circ\text{C} < T_J < 125^\circ\text{C}$ , unless otherwise specified)

PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Output Voltage	$T_J = 25^\circ\text{C}$	7.7	8.0	8.3	V
Line Regulation	$T_J = 25^\circ\text{C}$ , $I_{OUT} = 100\text{ mA}$		12	80	mV
	$10.5\text{ V} \leq V_{IN} \leq 25\text{ V}$		5.0	40	mV
Load Regulation	$11\text{ V} \leq V_{IN} \leq 17\text{ V}$				
	$T_J = 25^\circ\text{C}$ , $I_{OUT} = 500\text{ mA}$		50	160	mV
	$10.5\text{ V} \leq V_{IN} \leq 25\text{ V}$		22	80	mV
	$11\text{ V} \leq V_{IN} \leq 17\text{ V}$		26	160	mV
Output Voltage	$T_J = 25^\circ\text{C}$ , $5\text{ mA} \leq I_{OUT} \leq 1.5\text{ A}$		9.0	80	mV
	$250\text{ mA} \leq I_{OUT} \leq 750\text{ mA}$		7.6	8.4	V
Output Voltage	$10.5\text{ V} \leq V_{IN} \leq 23\text{ V}$ , $5\text{ mA} \leq I_{OUT} \leq 1.0\text{ A}$ , $p \leq 15\text{ W}$				
Quiescent Current	$T_J = 25^\circ\text{C}$		4.3	8.0	mA
Quiescent Current Change	$10.5\text{ V} \leq V_{IN} \leq 25\text{ V}$			1.0	mA
	$5\text{ mA} \leq I_{OUT} \leq 1.5\text{ A}$			0.5	mA
Output Noise Voltage	$T_A = 25^\circ\text{C}$ , $10\text{ Hz} \leq f \leq 100\text{ kHz}$		52		$\mu\text{V}$
Long Term Stability				32	mV
Ripple Rejection	$I_{OUT} = 20\text{ mA}$ , $f = 120\text{ Hz}$		62		dB
Dropout Voltage	$I_{OUT} = 1\text{ A}$ , $T_J = 25^\circ\text{C}$		2.0		V
Output Resistance	$I_{OUT} = 500\text{ mA}$		40		$\text{m}\Omega$
Short Circuit Current Limit	$T_J = 25^\circ\text{C}$		450		mA
Average Temperature Coefficient of Output Voltage	$I_{OUT} = 5\text{ mA}$ , $0^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$		-1.0		$\text{mV}/^\circ\text{C}$

**$\mu$ A7812**

**ELECTRICAL CHARACTERISTICS** ( $V_{IN} = 19\text{ V}$ ,  $I_{OUT} = 500\text{ mA}$ ,  $0^\circ\text{C} < T_J < 125^\circ\text{C}$ , unless otherwise specified)

PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Output Voltage	$T_J = 25^\circ\text{C}$	11.5	12.0	12.5	V
Line Regulation	$T_J = 25^\circ\text{C}$ , $I_{OUT} = 100\text{ mA}$		13	120	mV
	$14.5\text{ V} \leq V_{IN} \leq 30\text{ V}$		6.0	60	mV
Load Regulation	$16\text{ V} \leq V_{IN} \leq 22\text{ V}$				
	$T_J = 25^\circ\text{C}$ , $I_{OUT} = 500\text{ mA}$		55	240	mV
	$14.5\text{ V} \leq V_{IN} \leq 30\text{ V}$		24	120	mV
	$16\text{ V} \leq V_{IN} \leq 22\text{ V}$		46	240	mV
Output Voltage	$T_J = 25^\circ\text{C}$ , $5\text{ mA} \leq I_{OUT} \leq 1.5\text{ A}$		17	120	mV
	$250\text{ mA} \leq I_{OUT} \leq 750\text{ mA}$		14.5	12.6	V
Output Voltage	$14.5\text{ V} \leq V_{IN} \leq 27\text{ V}$ , $5\text{ mA} \leq I_{OUT} \leq 1.0\text{ A}$ , $p \leq 15\text{ W}$				
Quiescent Current	$T_J = 25^\circ\text{C}$		4.4	8.0	mA
Quiescent Current Change	$14.5\text{ V} \leq V_{IN} \leq 30\text{ V}$			1.0	mA
	$5\text{ mA} \leq I_{OUT} \leq 1.5\text{ A}$			0.5	mA
Output Noise Voltage	$T_A = 25^\circ\text{C}$ , $10\text{ Hz} \leq f \leq 100\text{ kHz}$		75		$\mu\text{V}$
Long Term Stability				48	mV
Ripple Rejection	$I_{OUT} = 20\text{ mA}$ , $f = 120\text{ Hz}$		61		dB
Dropout Voltage	$I_{OUT} = 1\text{ A}$ , $T_J = 25^\circ\text{C}$		2.0		V
Output Resistance	$I_{OUT} = 500\text{ mA}$		75		$\text{m}\Omega$
Short Circuit Current Limit	$T_J = 25^\circ\text{C}$		350		mA
Average Temperature Coefficient of Output Voltage	$I_{OUT} = 5\text{ mA}$ , $0^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$		-2.0		$\text{mV}/^\circ\text{C}$

**FAIRCHILD LINEAR INTEGRATED CIRCUITS •  $\mu$ A7800 SERIES**

**$\mu$ A7815**

**ELECTRICAL CHARACTERISTICS** ( $V_{IN} = 23$  V,  $I_{OUT} = 500$  mA,  $0^\circ\text{C} < T_J < 125^\circ\text{C}$ , unless otherwise specified)

PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Output Voltage	$T_J = 25^\circ\text{C}$	14.4	15.0	15.6	V
Line Regulation	$T_J = 25^\circ\text{C}$ , $I_{OUT} = 100$ mA		14	150	mV
	$17.5\text{ V} \leq V_{IN} \leq 30\text{ V}$		6.0	75	mV
Load Regulation	$T_J = 25^\circ\text{C}$ , $I_{OUT} = 500$ mA		57	300	mV
	$17.5\text{ V} \leq V_{IN} \leq 30\text{ V}$		27	150	mV
	$20\text{ V} \leq V_{IN} \leq 26\text{ V}$		68	300	mV
Output Voltage	$T_J = 25^\circ\text{C}$ , $5\text{ mA} \leq I_{OUT} \leq 1.5\text{ A}$		25	150	mV
	$250\text{ mA} \leq I_{OUT} \leq 750\text{ mA}$	14.25		15.75	V
Quiescent Current	$17.5\text{ V} \leq V_{IN} \leq 30\text{ V}$		4.4	8.0	mA
	$p \leq 15\text{ W}$				
Quiescent Current Change	$T_J = 25^\circ\text{C}$			1.0	mA
	$17.5\text{ V} \leq V_{IN} \leq 30\text{ V}$			0.5	mA
Output Noise Voltage	$5\text{ mA} \leq I_{OUT} \leq 1.5\text{ A}$		90		$\mu\text{V}$
Long Term Stability	$T_A = 25^\circ\text{C}$ , $10\text{ Hz} \leq f \leq 100\text{ kHz}$			60	mV
Ripple Rejection	$I_{OUT} = 20\text{ mA}$ , $f = 120\text{ Hz}$		60		dB
Dropout Voltage	$I_{OUT} = 1\text{ A}$ , $T_J = 25^\circ\text{C}$		2.0		V
Output Resistance	$I_{OUT} = 500\text{ mA}$		95		$\text{m}\Omega$
Short Circuit Current Limit	$T_J = 25^\circ\text{C}$		230		mA
Average Temperature Coefficient of Output Voltage	$I_{OUT} = 5\text{ mA}$ , $0^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$		-2.0		$\text{mV}/^\circ\text{C}$

**$\mu$ A7818**

**ELECTRICAL CHARACTERISTICS** ( $V_{IN} = 27$  V,  $I_{OUT} = 500$  mA,  $0^\circ\text{C} < T_J < 125^\circ\text{C}$ , unless otherwise specified)

PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Output Voltage	$T_J = 25^\circ\text{C}$	17.3	18.0	18.7	V
Line Regulation	$T_J = 25^\circ\text{C}$ , $I_{OUT} = 100$ mA		25	180	mV
	$21\text{ V} \leq V_{IN} \leq 33\text{ V}$		10	90	mV
Load Regulation	$T_J = 25^\circ\text{C}$ , $I_{OUT} = 500$ mA		90	360	mV
	$21\text{ V} \leq V_{IN} \leq 33\text{ V}$		50	180	mV
	$24\text{ V} \leq V_{IN} \leq 30\text{ V}$		110	360	mV
Output Voltage	$T_J = 25^\circ\text{C}$ , $5\text{ mA} \leq I_{OUT} \leq 1.0\text{ A}$		55	180	mV
	$250\text{ mA} \leq I_{OUT} \leq 750\text{ mA}$	17.1		18.9	V
Quiescent Current	$21\text{ V} \leq V_{IN} \leq 33\text{ V}$		4.5	8.0	mA
	$p \leq 15\text{ W}$				
Quiescent Current Change	$T_J = 25^\circ\text{C}$			1.0	mA
	$21\text{ V} \leq V_{IN} \leq 33\text{ V}$			0.5	mA
Output Noise Voltage	$5\text{ mA} \leq I_{OUT} \leq 1.0\text{ A}$		110		$\mu\text{V}$
Long Term Stability	$T_A = 25^\circ\text{C}$ , $10\text{ Hz} \leq f \leq 100\text{ kHz}$			72	mV
Ripple Rejection	$I_{OUT} = 20\text{ mA}$ , $f = 120\text{ Hz}$		59		dB
Dropout Voltage	$I_{OUT} = 1\text{ A}$ , $T_J = 25^\circ\text{C}$		2.0		V
Output Resistance	$I_{OUT} = 500\text{ mA}$		110		$\text{m}\Omega$
Short Circuit Current Limit	$T_J = 25^\circ\text{C}$		200		mA
Average Temperature Coefficient of Output Voltage	$I_{OUT} = 5\text{ mA}$ , $0^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$		-1.0		$\text{mV}/^\circ\text{C}$

# FAIRCHILD LINEAR INTEGRATED CIRCUITS • $\mu$ A7800 SERIES

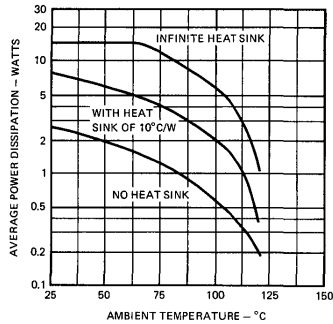
## $\mu$ A7824

**ELECTRICAL CHARACTERISTICS** ( $V_{IN} = 33\text{ V}$ ,  $I_{OUT} = 500\text{ mA}$ ,  $0^\circ\text{C} < T_J < 125^\circ\text{C}$ , unless otherwise specified)

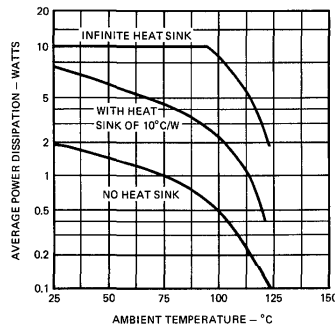
PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Output Voltage	$T_J = 25^\circ\text{C}$	23.0	24.0	35.0	V
Line Regulation	$T_J = 25^\circ\text{C}$ , $I_{OUT} = 100\text{ mA}$		31	240	mV
	$27\text{ V} \leq V_{IN} \leq 38\text{ V}$		14	120	mV
	$30\text{ V} \leq V_{IN} \leq 36\text{ V}$				
	$T_J = 25^\circ\text{C}$ , $I_{OUT} = 500\text{ mA}$		118	480	mV
Load Regulation	$27\text{ V} \leq V_{IN} \leq 38\text{ V}$		70	240	mV
	$30\text{ V} \leq V_{IN} \leq 36\text{ V}$				
	$T_J = 25^\circ\text{C}$ , $5\text{ mA} \leq I_{OUT} \leq 1.0\text{ A}$		150	480	mV
Output Voltage	$250\text{ mA} \leq I_{OUT} \leq 750\text{ mA}$		85	240	mV
	$27\text{ V} \leq V_{IN} \leq 38\text{ V}$ , $5\text{ mA} \leq I_{OUT} \leq 1.0\text{ A}$	22.8		25.2	V
Quiescent Current	$p \leq 15\text{ W}$				
Quiescent Current Change	$T_J = 25^\circ\text{C}$		4.6	8.0	mA
Output Noise Voltage	$27\text{ V} \leq V_{IN} \leq 38\text{ V}$			1.0	mA
	$5\text{ mA} \leq I_{OUT} \leq 1.0\text{ A}$			0.5	mA
Long Term Stability	$T_A = 25^\circ\text{C}$ , $10\text{ Hz} \leq f \leq 100\text{ kHz}$		170		$\mu\text{V}$
Ripple Rejection				96	mV
Dropout Voltage	$I_{OUT} = 20\text{ mA}$ , $f = 120\text{ Hz}$		56		dB
Output Resistance	$I_{OUT} = 1\text{ A}$ , $T_J = 25^\circ\text{C}$		2.0		V
Short Circuit Current Limit	$I_{OUT} = 500\text{ mA}$		150		$\text{m}\Omega$
Average Temperature Coefficient of Output Voltage	$T_J = 25^\circ\text{C}$		150		mA
	$I_{OUT} = 5\text{ mA}$ , $0^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$	-1.0			$\text{mV}/^\circ\text{C}$

### TYPICAL PERFORMANCE CURVES

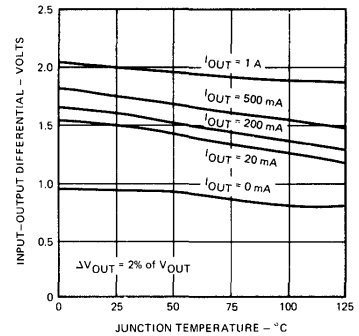
**MAXIMUM AVERAGE POWER DISSIPATION AS A FUNCTION OF AMBIENT TEMPERATURE (TO-3 PACKAGE)**



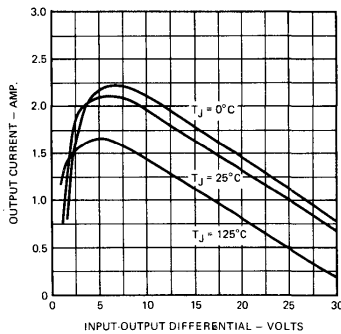
**MAXIMUM AVERAGE POWER DISSIPATION AS A FUNCTION OF AMBIENT TEMPERATURE (TO-220 PACKAGE)**



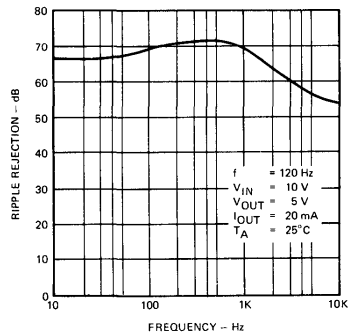
**DROPOUT VOLTAGE AS A FUNCTION OF JUNCTION TEMPERATURE**



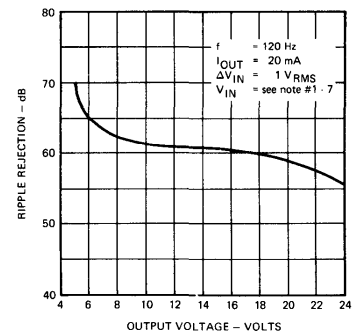
**PEAK OUTPUT CURRENT AS A FUNCTION OF INPUT-OUTPUT DIFFERENTIAL VOLTAGE**



**RIPPLE REJECTION AS A FUNCTION OF FREQUENCY**

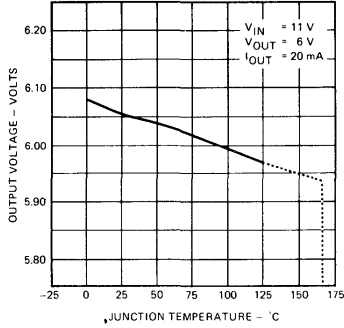


**RIPPLE REJECTION AS A FUNCTION OF OUTPUT VOLTAGES**

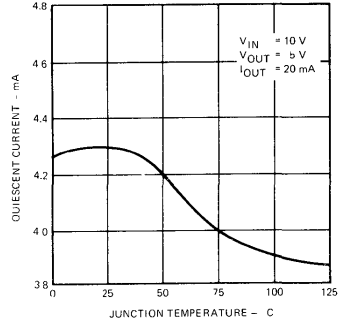


TYPICAL PERFORMANCE CURVES (cont'd)

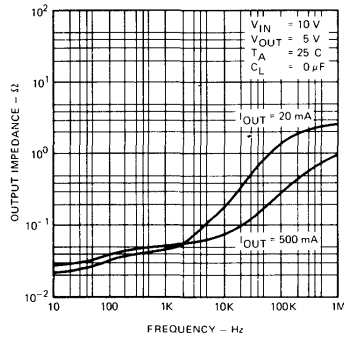
OUTPUT VOLTAGE  
AS A FUNCTION OF JUNCTION TEMPERATURE



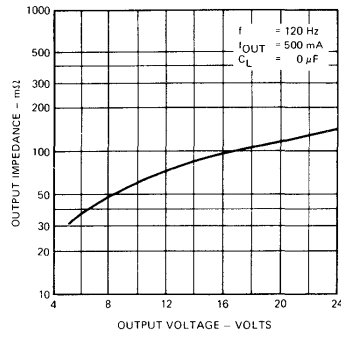
QUIESCENT CURRENT  
AS A FUNCTION OF TEMPERATURE



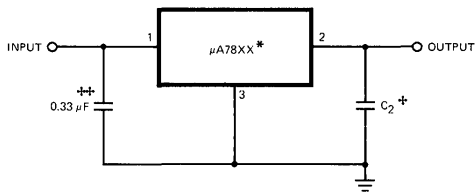
OUTPUT IMPEDANCE  
AS A FUNCTION OF FREQUENCY



OUTPUT IMPEDANCE  
AS A FUNCTION OF OUTPUT VOLTAGE

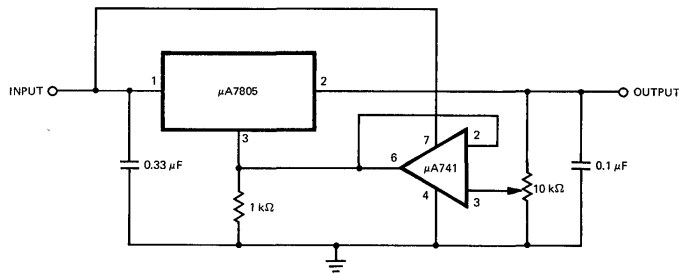


APPLICATIONS



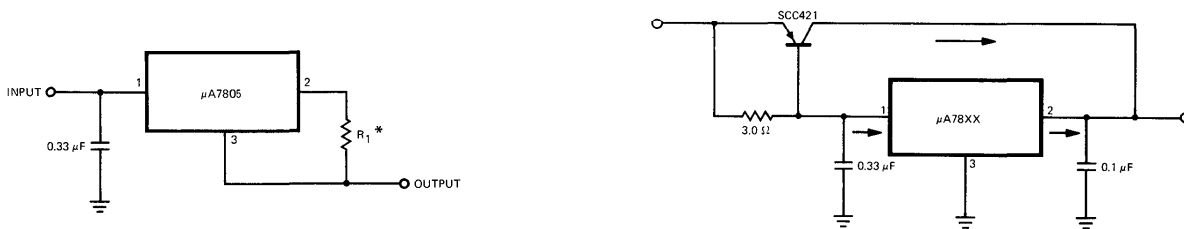
- NOTES:  
 \*To specify an output voltage, substitute voltage value for "XX".  
 †Although no output capacitor is needed for stability, it does improve transient response.  
 †† Required if regulator is located an appreciable distance from power supply filter.

FIXED OUTPUT REGULATOR



$V_{OUT}$ , 7 V to 20 V  
 $V_{IN} - V_{OUT} \geq 2$  V

ADJUSTABLE OUTPUT REGULATOR – HIGH LINE REGULATION



\* $R_1$  determines output current.

CURRENT REGULATOR

HIGH CURRENT VOLTAGE REGULATOR