## MCT2, MCT2E OPTOCOUPLERS

SOES023 - MARCH 1983 - REVISED OCTOBER 1995

### COMPATIBLE WITH STANDARD TTL INTEGRATED CIRCUITS

- Gallium Arsenide Diode Infrared Source Optically Coupled to a Silicon npn Phototransistor
- High Direct-Current Transfer Ratio
- Base Lead Provided for Conventional Transistor Biasing
- High-Voltage Electrical Isolation . . . 1.5-kV, or 3.55-kV Rating
- Plastic Dual-In-Line Package
- High-Speed Switching:
  t<sub>r</sub> = 5 μs, t<sub>f</sub> = 5 μs Typical
- Designed to be Interchangeable with General Instruments MCT2 and MCT2E



MCT2 OR MCT2E ... PACKAGE



### absolute maximum ratings at 25°C free-air temperature (unless otherwise noted)<sup>†</sup>

Input-to-output voltage: MCT2	±1.5 kV
MCT2E	± 3.55 kV
Collector-base voltage	
Collector-emitter voltage (see Note 1)	
Emitter-collector voltage	
Emitter-base voltage	
Input-diode reverse voltage	
Input-diode continuous forward current	60 mA
Input-diode peak forward current ( $t_w \le 1$ ns, PRF $\le 300$ Hz)	
Continuous power dissipation at (or below) 25°C free-air temperature:	
Infrared-emitting diode (see Note 2)	
Phototransistor (see Note 2)	
Total, infrared-emitting diode plus phototransistor (see Note 3)	
Operating free-air temperature range, T <sub>A</sub>	–55°C to 100°C
Storage temperature range, T <sub>stg</sub>	–55°C to 150°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	260°C

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. This value applies when the base-emitter diode is open-circulated.

2. Derate linearly to 100  $^\circ\text{C}$  free-air temperature at the rate of 2.67 mW/°C.

3. Derate linearly to 100  $^{\circ}\text{C}$  free-air temperature at the rate of 3.33 mW/ $^{\circ}\text{C}.$ 



SOES023 - MARCH 1983 - REVISED OCTOBER 1995

### electrical characteristics at 25°C free-air temperature (unless otherwise noted)

PARAMETER			TEST CONDITIONS			MIN	TYP	MAX	UNIT
V(BR)CBO	Collector-base breakdown vo	I <sub>C</sub> = 10 μA,	I <sub>E</sub> = 0,	IF = 0	70			V	
V(BR)CEO	Collector-emitter breakdown	IC =1 mA,	$I_{B} = 0,$	IF = 0	30			V	
V(BRECO)	Emitter-collector breakdown	voltage	I <sub>E</sub> = 100 μA,	I <sub>B</sub> = 0,	IF = 0	7			V
I <sub>R</sub>	Input diode static reverse cu	V <sub>R</sub> = 3 V					10	μA	
I <sub>C(on)</sub>	On-state collector current	Phototransistor operation	V <sub>CE</sub> = 10 V,	I <sub>B</sub> = 0,	I <sub>F</sub> = 10 mA	2	5		mA
		Photodiode operation	V <sub>CB</sub> = 10 V,	I <sub>E</sub> = 0,	I <sub>F</sub> = 10 mA		20		μA
I <sub>C(off)</sub>	Off-state collector current	Phototransistor operation	V <sub>CE</sub> = 10 V,	I <sub>B</sub> = 0,	IF = 0		1	50	nA
		Photodiode operation	V <sub>CB</sub> = 10 V,	I <sub>E</sub> = 0,	IF = 0		0.1	20	nA
			V <sub>CE</sub> = 5 V,	MCT2			250		
HFE Transistor static forward curre		ent transfer ratio	$I_{C} = 100 \ \mu A,$ $I_{F} = 0$	MCT2E		100	300		
۷ <sub>F</sub>	Input diode static forward voltage		I <sub>F</sub> = 20 mA			1.25	1.5	V	
V <sub>CE(sat)</sub>	Collector-emitter saturation voltage		I <sub>C</sub> = 2 mA,	I <sub>B</sub> = 0,	I <sub>F</sub> = 16 mA		0.25	4	V
rIO	Input-to-output internal resistance		$V_{\text{in-out}} = \pm 1.8 \\ \pm 3.8 \\ \text{See Note 4}$	5 kV for M 55 kV for N	CT2, MCT2E,	1011			Ω
C <sub>io</sub>	Input-to-output capacitance		V <sub>in-out</sub> = 0, See Note 4	f = 1 MH	z,		1		pF

NOTE 4: These parameters are measured between both input diode leads shorted together and all the phototransistor leads shorted together.

### switching characteristics

PARAMETER			TEST CONDITIONS	MIN	TYP	MAX	UNIT
t <sub>r</sub>	Rise time	Phototransistor operation	$V_{CC} = 10 V$ , $I_{C(on)} = 2 mA$ ,		Б		
tf	Fall time		$R_{L} = 100 \Omega$ , See Test Circuit A of Figure		5		μs
tr	Rise time	Photodiada aparatian	V <sub>CC</sub> = 10 V, I <sub>C(on)</sub> 20 μA,		1		
t <sub>f</sub>	Fall time	Photodiode operation	$R_L = 1 k\Omega$ , See Test Circuit B of Figure		I		μs



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SOES023 - MARCH 1983 - REVISED OCTOBER 1995



### PARAMETER MEASUREMENT INFORMATION

NOTES: A. The input waveform is supplied by a generator with the following characteristics:  $Z_0 = 50 \Omega$ ,  $t_r \le 15$  ns, duty cycle  $\approx 1\%$ ,  $t_W = 100 \ \mu s.$ B. The output waveform is monitored on an oscilloscope with the following characteristics:  $t_r \le 12 \ ns, \ R_{in} \ge 1 \ M\Omega, \ C_{in} \le 20 \ pF.$ 

### **Figure 1. Switching Times**



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SOES023 - MARCH 1983 - REVISED OCTOBER 1995

### **TYPICAL CHARACTERISTICS**



Figure 2





Figure 3



Figure 4



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SOES023 - MARCH 1983 - REVISED OCTOBER 1995

### **MECHANICAL INFORMATION**

The package consists of a gallium-arsenide infrared-emitting diode and an npn silicon phototransistor mounted on a 6-lead frame encapsulated within an electrically nonconductive plastic compound. The case can withstand soldering temperature with no deformation and device performance characteristics remain stable when operated in high-humidity conditions. Unit weight is approximately 0.52 grams.



- NOTES: A. Leads are within 0,13 (0.005) radius of true position (T.P.) with maximum material condition and unit installed.
  - B. Pin 1 identified by index dot.
  - C. Terminal connections:
    - 1. Anode (part of the infrared-emitting diode)
    - 2. Cathode (part of the infrared-emitting diode)
    - 3. No internal connection
    - 4. Emitter (part of the phototransistor)
    - 5. Collector (part of the phototransistor)
    - 6. Base (part of the phototransistor)
  - D. The dimensions given fall within JEDEC MO-001 AM dimensions.
  - E. All linear dimensions are given in millimeters and parenthetically given in inches.

### Figure 5. Mechanical Information



### PACKAGING INFORMATION

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
MCT2	OBSOLETE	PDIP	Ν	6	TBD	Call TI	Call TI
MCT2E	OBSOLETE	PDIP	N	6	TBD	Call TI	Call TI

<sup>(1)</sup> The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details. TBD: The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

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<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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