TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSIV)

2SK4207

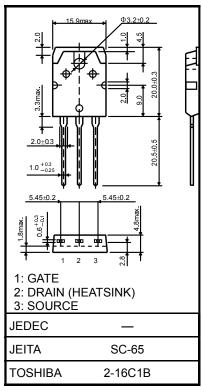
Swiching Regulator Applications

Unit: mm

- Low drain-source ON-resistance: $R_{DS (ON)} = 0.78 \Omega (typ.)$
- High forward transfer admittance: |Y_{fS}| = 11 S (typ.)
- Low leakage current: I_{DSS} = 100 μA (max) (V_{DS} = 720 V)
- Enhancement mode: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

| Characteristics | | Symbol | Rating | Unit | |
|--------------------------------------|------------------------|------------------|------------|------|--|
| Drain-source voltage | | V_{DSS} | 900 | V | |
| Drain-gate voltage (R | _{GS} = 20 kΩ) | V_{DGR} | 900 | V | |
| Gate-source voltage | | V_{GSS} | ±30 | ٧ | |
| Drain current | DC (Note 1) | I _D | 13 | А | |
| | Pulse (Note 1) | I _{DP} | 39 | А | |
| Drain power dissipation | n (Tc = 25°C) | P_{D} | 150 | W | |
| Single pulse avalanche | e energy (Note 2) | E _{AS} | 491 | mJ | |
| Avalanche current | | I _{AR} | 13 | Α | |
| Repetitive avalanche energy (Note 3) | | E _{AR} | 15 | mJ | |
| Channel temperature | | T _{ch} | 150 | °C | |
| Storage temperature ra | ange | T _{stg} | −55 to 150 | °C | |



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

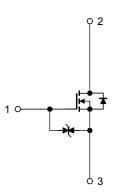
| Characteristics | Symbol | Max | Unit |
|--|------------------------|-------|------|
| Thermal resistance, channel to case | R _{th (ch-c)} | 0.833 | °C/W |
| Thermal resistance, channel to ambient | R _{th (ch-a)} | 50 | °C/W |



Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 5.3 mH, R_G = 25 Ω , I_{AR} = 13 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



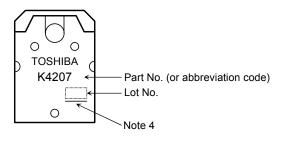
Electrical Characteristics (Ta = 25°C)

| Charac | cteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|---|-----------------|----------------------|---|-----|------|------|-------|
| Gate leakage current | | I _{GSS} | V _{GS} = ±30 V, V _{DS} = 0 V | _ | _ | ±10 | μΑ |
| Gate-source breakdown voltage | | V (BR) GSS | I _G = ±10 μA, V _{DS} = 0 V | | _ | _ | V |
| Drain cut-off cu | rrent | I _{DSS} | V _{DS} = 720 V, V _{GS} = 0 V | _ | _ | 100 | μΑ |
| Drain-source br | eakdown voltage | V (BR) DSS | I _D = 10 mA, V _{GS} = 0 V | 900 | _ | _ | V |
| Gate threshold | voltage | V _{th} | V _{DS} = 10 V, I _D = 1 mA | 2.0 | _ | 4.0 | V |
| Drain-source O | N-resistance | R _{DS} (ON) | V _{GS} = 10 V, I _D = 6.5 A | _ | 0.78 | 0.95 | Ω |
| Forward transfe | r admittance | Y _{fs} | V _{DS} = 10 V, I _D = 6.5 A | 5.0 | 11 | _ | S |
| Input capacitance | | C _{iss} | V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz | _ | 2790 | _ | pF |
| Reverse transfer capacitance | | C _{rss} | | _ | 25 | _ | |
| Output capacitance | | Coss | | _ | 300 | _ | |
| Switching time | Rise time | t _r | $V_{GS} = \frac{10V}{0V}$ $V_{GS} = \frac{10}{0V}$ V_{OUT} V_{OUT} $V_{DD} = 400V$ $V_{DD} = 400V$ $V_{DU} = 10\mu s$ | _ | 53 | _ | |
| | Turn-on time | t _{on} | | _ | 88 | | ns |
| | Fall time | t _f | | 1 | 43 | l | . 115 |
| | Turn-off time | t _{off} | | _ | 165 | _ | |
| Total gate charge (gate-source plus gate-drain) | | Qg | | _ | 45 | _ | nC |
| Gate-source charge | | Qgs | $V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 13 \text{ A}$ | _ | 32 | _ | |
| Gate-drain ("miller") Charge | | Q_{gd} | <u> </u> | | 13 | _ | |

Source-Drain Ratings and Characteristics (Ta = 25°C)

| Characteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|---|------------------|---|-----|------|------|------|
| Continuous drain reverse current (Note 1) | I _{DR} | _ | _ | _ | 13 | Α |
| Pulse drain reverse current (Note 1) | I _{DRP} | _ | _ | _ | 39 | Α |
| Forward voltage (diode) | V _{DSF} | I _{DR} = 13 A, V _{GS} = 0 V | _ | _ | -1.7 | V |
| Reverse recovery time | t _{rr} | I _{DR} = 13 A, V _{GS} = 0 V | _ | 1400 | _ | ns |
| Reverse recovery charge | Q _{rr} | dl _{DR} / dt = 100 A /μs | _ | 24 | _ | μС |

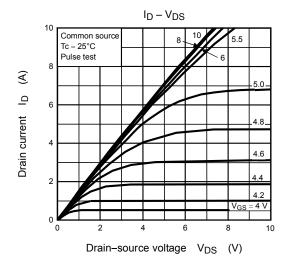
Marking

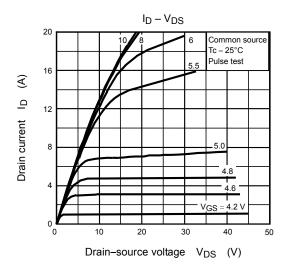


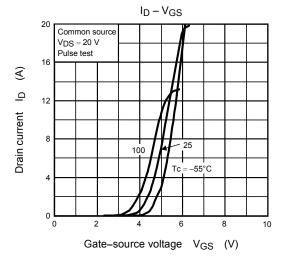
Note 4: A line under a Lot No. identifies the indication of product Labels.

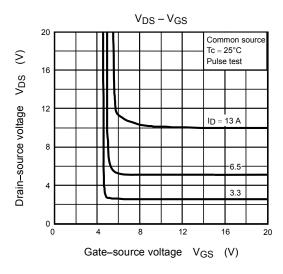
[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

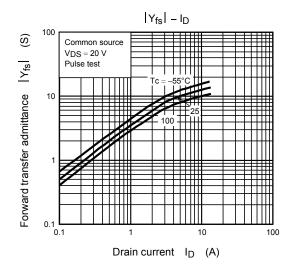
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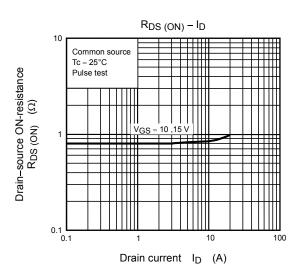


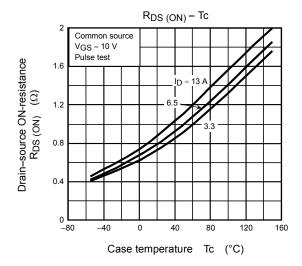


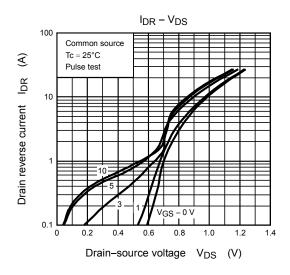


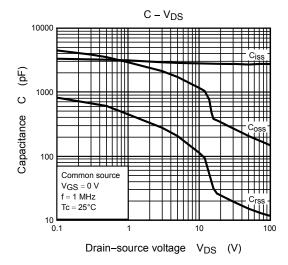


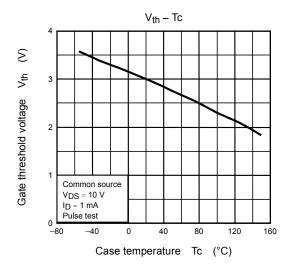


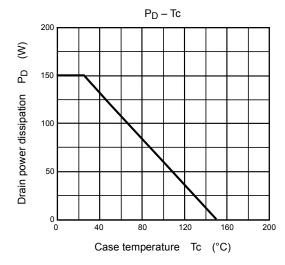


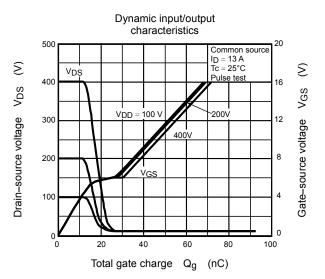


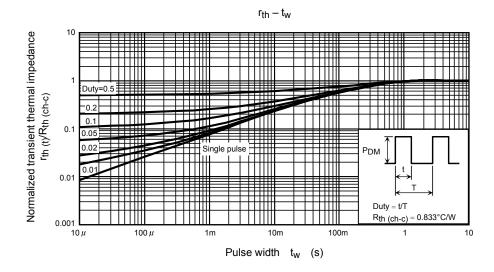




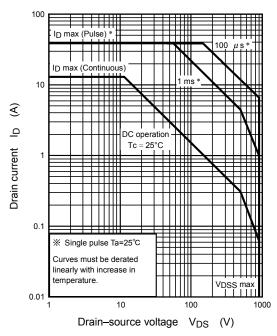


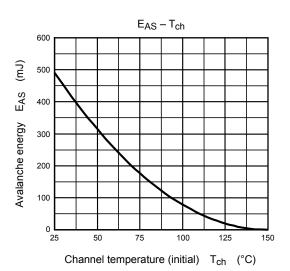


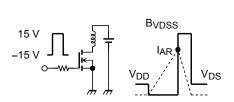




SAFE OPERATING AREA







$$\begin{aligned} &R_G = 25~\Omega \\ &V_{DD} = 90~V,~L = 5.3~mH \end{aligned}$$

Test circuit

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - VDD} \right)$$

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